

# Midium Power Transistors (-30V / -1A)

## **2SAR293P**

#### Structure

PNP Silicon epitaxial planar transistor

#### Features

Low saturation voltage

 $V_{CE (sat)} = -0.35V (Max.) (I_C / I_B = -500mA / -25mA)$ 

#### Applications

Driver

## Packaging specifications

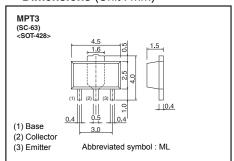
Туре	Package	MPT3
	Code	T100
	Basic ordering unit (pieces)	1000

### ●Absolute maximum ratings (Ta=25°C)

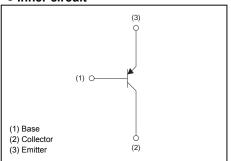
Parameter		Symbol	Limits	Unit
Collector-base voltage		$V_{CBO}$	-30	V
Collector-emitter voltage		$V_{CEO}$	-30	V
Emitter-base voltage		$V_{EBO}$	-6	V
Collector current	DC	Ic	-1	Α
	Pulsed	I <sub>CP</sub> *1	-2	Α
Power dissipation		P <sub>D</sub> *2	0.5	W
		P <sub>D</sub> *3	2.0	W
Junction temperature		Tj	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to 150	°C

<sup>\*1</sup> Pw=10ms, Single Pulse

#### Dimensions (Unit : mm)



#### • Inner circuit



<sup>\*2</sup> Mounted on a recommended land

<sup>\*3</sup> Mounted on a 40×40×0.7 [mm] ceramic substrate

## ●Electrical characteristic (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-emitter breakdown voltage	$BV_{CEO}$	-30	-	-	V	I <sub>C</sub> = -1mA	
Collector-base breakdown voltage	BV <sub>CBO</sub>	-30	-	-	V	I <sub>C</sub> = -10μA	
Emitter-base breakdown voltage	$BV_{EBO}$	-6	-	-	V	I <sub>E</sub> = -10μA	
Collector cut-off current	I <sub>CBO</sub>	-	-	-100	nA	V <sub>CB</sub> = -30V	
Emitter cut-off current	I <sub>EBO</sub>	-	-	-100	nA	V <sub>EB</sub> = -6V	
Collector-emitter staturation voltage	V <sub>CE(sat)</sub> *1	1	-150	-350	mV	$I_C$ = -500mA, $I_B$ = -25mA	
DC current gain	h <sub>FE</sub>	270	-	680	-	$V_{CE}$ = -2V, $I_{C}$ = -100mA	
Transition frequency	f <sub>T</sub> *1	-	320	-	MHz	V <sub>CE</sub> = -2V, I <sub>E</sub> =100mA f=100MHz	
Collector output capacitance	C <sub>ob</sub>	-	7	-	pF	$V_{CB}$ = -10V, $I_{E}$ =0A f=1MHz	
Turn-on time	t <sub>on</sub> *2	-	60	-	ns	I <sub>C</sub> = -500mA, I <sub>B1</sub> = -25mA I <sub>B2</sub> =25mA, V <sub>CC</sub> ≃-10V	
Storage time	t <sub>stg</sub> *2	-	160	_	ns		
Fall time	t <sub>f</sub> *2	-	50	-	ns		

<sup>\*1</sup> Pulsed

<sup>\*2</sup> See switching time test circuit

#### ●Electrical characteristic curves (Ta=25°C)

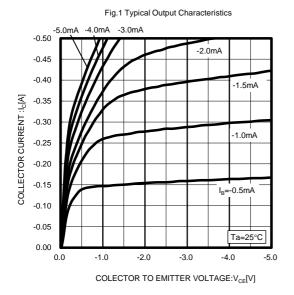


Fig.3 DC Current Gain vs. Collector Current( II )

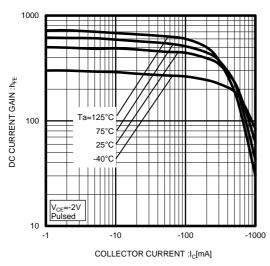


Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current( II )

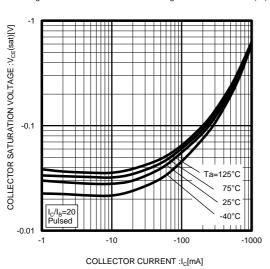


Fig.2 DC Current Gain vs. Collector Current( I )

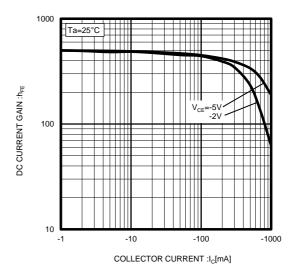


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current( I )

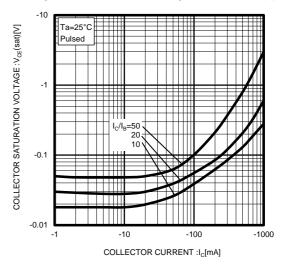
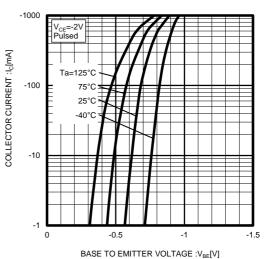
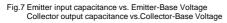


Fig.6 Ground Emitter Propagation Characteristics





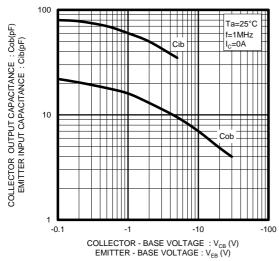


Fig9. SAFE OPERATING AREA

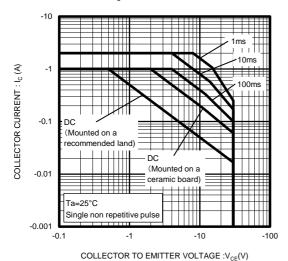
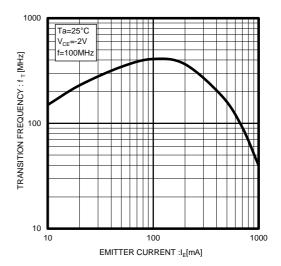
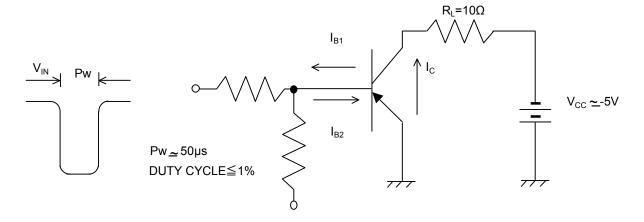
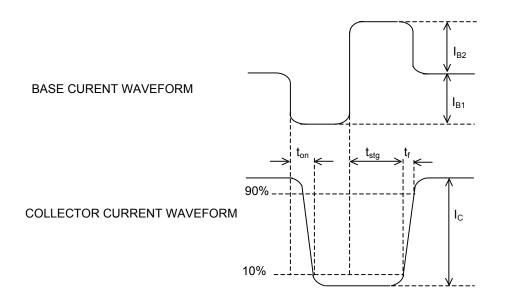


Fig8. Gain Bandwidth Productvs. Emitter Current



## • Switching time test circuit





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